

April 27, 2006



CUSTOMER NUMBER

22887

PATENT TRADEMARK OFFICE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

RE: U.S. Patent Application Serial No.: 09/779,382  
Filed: February 8, 2001  
Entitled: MULTISTANDARD VIDEO DECODER AND DECOMPRESSION SYSTEM  
FOR PROCESSING ENCODED BIT STREAMS INCLUDING CIRCUITRY FOR  
SELECTING TOKENS AND METHODS RELATING THERETO  
Art Unit: 2154  
Confirmation No.: 7169  
Inventor: Adrian P. Wise  
Our File No.: 94100422(EP)USC1X1C1D11 PDDD

Dear Sir:

Enclosed for filing in the above-referenced application are the following documents:

1. Communication - Information Disclosure Statement, with PTO-1449 form and references;
2. Cover Letter;
3. Return Receipt Postcard (Document Control No. A48OHS);
4. Certificate of First Class Transmittal.

Please indicate on the enclosed pre-addressed, postage pre-paid postcard the date of receipt of the enclosed materials and mail the postcard to acknowledge receipt of this transmittal.

Very truly yours,

DISCOVISION ASSOCIATES

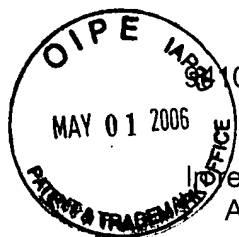
*Micah Goldsmith / tz*

Micah P. Goldsmith, Reg. No. 43,638  
Senior Patent Prosecution Attorney  
INTELLECTUAL PROPERTY DEVELOPMENT

MG:bp  
Enclosures

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CERTIFICATE OF FIRST CLASS TRANSMISSION	
I hereby certify that this correspondence is being electronically transmitted to the U.S. Patent and Trademark Office on:	
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94100422(EP)USC1X1C1D11 PDDD

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:  
Adrian P. Wise et al.

Filed: February 8, 2001

For: MULTISTANDARD VIDEO DECODER  
AND DECOMPRESSION SYSTEM FOR  
PROCESSING ENCODED BIT STREAMS  
INCLUDING CIRCUITRY FOR SELECTING  
TOKENS AND METHODS RELATING  
THERETO

§  
§ Application No.: 09/779,382  
§  
§ Art Unit: 2154  
§  
§ Confirmation No: 7169  
§  
§ Examiner: NGUYEN, Dustin  
§  
§ Attorney Docket No.  
§ 94100422(EP)USC1X1C1D11 PDDD  
§

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

MAIL STOP AMENDMENT

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450  
Sir:

As requested by the Examiner in the Office Action dated January 27, 2006 and in complying with the duty of disclosure set forth in 37 CFR §1.56, Applicant resubmits the foreign patents and published documents as mentioned in the IDS filed with the USPTO on 02/08/2001. Form PTO-1449 and a copy of all the documents are attached hereto.

The Commissioner is hereby authorized to charge payment of the fee required by 37 C.F.R. §1.17(i), 37 C.F.R. §1.17(p), and any fees associated with this communication or credit any overpayment to Deposit Account No. 04-1175.

05/01/2006 NGUYEN1 00000104 041175 09779382  
01 FC:1806 180.00 DA

Respectfully submitted,

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Date: 4/26/06

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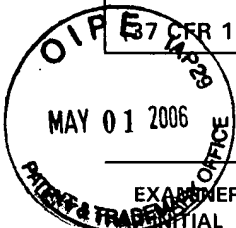
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94100422(EP)USC1X1C1D11  
PDDDSERIAL NO.  
09/779,382INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT

(Use several sheets if necessary)

APPLICANT:  
ADRIAN P. WISE et al.EXAMINER:  
Dustin NGUYENFILING DATE:  
February 8, 2001Art Unit: 2154  
Confirmation: 7169

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
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## FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION YES   NO
		0196911	08/10/86	EP			
		0255767	10/02/88	EP			
		0468480	29/01/92	EP			
		0572262	01/12/93	EP			
		0572263	01/12/93	EP			
		0576749	05/01/94	EP			
		0589 734	30/03/94	EP			
		0618728	05/10/94	EP			
		0639032	15/02/95	EP			
		2 045 035	22/10/80	GB			
		2 059 724	23/04/81	GB			
		2 171 578	28/08/86	GB			
		2 194 085	24/02/88	GB			
		2 268 035	22/12/93	GB			
		2 269 070	26/01/94	GB			

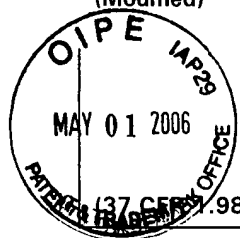
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EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION YES   NO
	9425935	11/10/94	WO			NO

## OTHER DOCUMENTS (Including Author, Title, Date\*\*, Relevant Pages, Place of Publication\*\*\*)

EXAMINER INITIAL	DOCUMENT IDENTIFICATION
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PATENT AND TRADEMARK OFFICE

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## APPLICANT:

ADRIAN P. WISE et al.

## EXAMINER:

Dustin NGUYEN

## FILING DATE:

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INITIAL

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APPROACH." IEEE TRANSACTION ON ACOUSTICS, SPEECH &amp; SIGNAL PROCESSING, VOL. 39, NO. 2, FEBRUARY 1991, IEEE PRESS, NEW YORK, US, PAGES 544-546.

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P. YIP, ET AL.: "DIT AND DIF ALGORITHM FOR DISCRETE SINE AND COSINE TRANSFORMS" PROCEEDINGS OF THE INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS, IEEE PRESS, NEW YORK, US, VOL. 2/3, 5 JUNE 1985, KYOTO, JP, PAGES 941-944.

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